

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Canceled)
2. (Previously Presented) A method for reducing interrupts while tracing an application in a data processing system, the method comprising:

receiving at a tracing function an indication that at least a portion of executable code from an application has been loaded into a memory block prior to execution of the portion of executable code; and

altering by the tracing function at least one operating-system-defined memory access protection parameter to allow read access to the memory block, and wherein the receiving step and the altering step are performed for each memory fault for the application.
3. (Original) The method of claim 2 wherein a memory fault includes a page fault or a segment fault.
4. (Canceled)
5. (Previously Presented) An apparatus for reducing interrupts while tracing an application in a data processing system, the apparatus comprising:

means for receiving at a tracing function an indication that at least a portion of executable code from an application has been loaded into a memory block prior to execution of the portion of executable code; and

means for altering by the tracing function at least one operating-system-defined memory access protection parameter to allow read access to the memory block, and wherein the receiving means and the altering means are activated for each memory fault for the application.
6. (Currently Amended) The ~~method~~ apparatus of claim 5 wherein a memory fault includes a page fault or a segment fault.
7. (Canceled)

8. (Previously Presented) A computer program product in a computer-readable medium for use in a data processing system for reducing interrupts while tracing an application, the computer program product comprising:

instructions for receiving at a tracing function an indication that at least a portion of executable code from an application has been loaded into a memory block prior to execution of the portion of executable code; and

instructions for altering by the tracing function at least one operating-system-defined memory access protection parameter to allow read access to the memory block, and wherein the receiving means and the altering means are activated for each memory fault for the application.

9. (Original) The computer program product of claim 8 wherein a memory fault includes a page fault or a segment fault.

10-42 (Canceled)